Latest news about Huawei micro-modules

Overview

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Key Takeaways

Huawei has unveiled breakthroughs in micro-module and chip technology, including the 1.4 nm Kirin chip with LogicFolding architecture and plans for 2 nm-class chips using DUV lithography.1. 1.4 nm Kirin Chip and LogicFolding Architecture

Huawei recently announced a new path in semiconductor development that bypasses traditional geometric scaling, introducing LogicFolding architecture and the Tau Scaling Law. This approach replaces Moore's Law with time scaling and 3D vertical stacking, allowing higher transistor density without relying on extreme ultraviolet (EUV) lithography . The first Kirin chipset using this architecture is expected to launch with Huawei's flagship devices in fall 2026, promising improved performance and efficiency .

2. Tau Scaling Law

The Tau Scaling Law is Huawei's proposed alternative to Moore's Law, focusing on 3D chip stacking rather than shrinking transistors. This method has been tested on over 381 chips across smartphones and AI applications over six years, demonstrating practical viability . Huawei claims this technology could achieve transistor densities equivalent to a 1.4 nm process by 2031, marking a significant milestone in semiconductor innovation .

3. 2 nm-Class Chip Development

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In parallel, Huawei is exploring 2 nm-class chip production using deep-ultraviolet (DUV) lithography instead of restricted EUV tools. A recently publicized patent outlines a Self-Aligned Quadruple Patterning (SAQP) technique that optimizes DUV exposures, potentially enabling ultra-dense chip designs without EUV machines . While technically ambitious, industry experts caution that commercial viability may be challenging due to yield and cost concerns .

4. Implications for Micro-Modules

These advancements in chip architecture and micro-module integration are expected to enhance Huawei's smartphone and AI hardware, allowing more compact, energy-efficient, and high-performance modules. The combination of LogicFolding, Tau Scaling Law, and DUV-based 2 nm-class techniques positions Huawei to maintain competitiveness despite global supply chain restrictions .

5. Broader Context

Huawei's micro-module innovations reflect a strategic push toward self-sufficiency in semiconductor technology, reducing reliance on foreign suppliers like TSMC and EUV equipment manufacturers. These developments are closely watched in the global tech industry, as they could influence AI chip wars, smartphone performance, and China's semiconductor capabilities . In summary, Huawei is actively advancing micro-module and chip technology through innovative architectures and alternative lithography methods, with near-term releases expected in 2026 and long-term goals targeting 1.4 nm and 2 nm-class chips by 2031 .

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Huawei is developing a true 5 nm production line without the need for EUV lithography machines. Furthermore, 3 nm

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Satellite images have revealed Huawei''s new advanced chip factory. Rumors suggest it will

Huawei''s new phone uses more China-made parts, memory chip

Huawei''s latest high-end phone features more Chinese suppliers, including a new flash memory storage chip and an

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Huawei plans to manufacture about 600,000 of its Ascend 910C chips in 2026, doubling output from 2025 levels.

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